

Electronic Acknowledgement Receipt

EFS ID:	3923943
Application Number:	10595303
International Application Number:	
Confirmation Number:	7114
Title of Invention:	Method and Device For Secure, Insulated and Electrically Conductive Assembling Of Treated Semiconductor Wafers
First Named Inventor/Applicant Name:	Roy Knechtel
Customer Number:	21967
Filer:	Jeff Vockrodt/Marcus Cunningham
Filer Authorized By:	Jeff Vockrodt
Attorney Docket Number:	60291.000048
Receipt Date:	11-SEP-2008
Filing Date:	19-MAY-2006
Time Stamp:	11:15:10
Application Type:	U.S. National Stage under 35 USC 371

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		IDS.PDF	179201	yes	4
			b1138046623f1c166da925bc5e5aefec192866ca		

Multipart Description/PDF files in .zip description					
Document Description			Start	End	
Information Disclosure Statement Letter			1	2	
Information Disclosure Statement (IDS) Filed (SB/08)			3	4	
Warnings:					
Information:					
2	Foreign Reference	GBpatapp.PDF	432339 1a9f5ee101ff16145c53ff733128c9ba45089	no	16
Warnings:					
Information:					
3	Foreign Reference	EURpatapp.PDF	357838 db736cafd23842a8f94cc530fca08ba2cc6a6884	no	7
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4	Foreign Reference	DE19616014.PDF	628903 a04b7a651d1787ac7aba1ca1cf66748f8f09096	no	7
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5	Foreign Reference	DE10141753.PDF	505480 33bc031a83ff1cc077186d27aa16873e1e459c	no	5
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7	Foreign Reference	DE3837300.PDF	461242 811541bba8937798a29c9da1320cc0a0488618	no	5
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8	NPL Documents	ISR.PDF	112208 c77a0f52996760728381c768486720b73c3ba00	no	3
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9	NPL Documents	Jin.PDF	383500 cd8055673b4178e8c3812333ac21b59afb7c15da	no	6
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10	NPL Documents	Kadar.PDF	1242359 36e99b319f8ba364ef8f5806cd7753acc58e1b	no	16
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11	Fee Worksheet (PTO-06)	fee-info.pdf	30403 1482c786ab4d5684827ae488702156bc015376	no	2
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